

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	49	257/e23.003.ccls.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT	OR	OFF	2008/01/15 18:22
L6	2249	257/777.ccls.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT	OR	OFF	2008/01/15 18:33
L7	38	((packag\$6 wir\$6 circuit) near (substrate board)) pwb pcb) and (((chip die dice) near2 stack\$6) with (spacer standoff interposer separat\$6))	FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/01/15 19:31
L8	270	((packag\$6 wir\$6 circuit) near (substrate board)) pwb pcb) same (((chip die dice) near2 stack\$6) with (spacer standoff interposer separat\$6))	US-PGPUB; USPAT	OR	ON	2008/01/15 20:21
L9	7063	257/686.ccls. 257/777.ccls. 257/778.ccls. 257/e23.003.ccls.	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT	OR	OFF	2008/01/15 20:00
L10	465	L9 and (((packag\$6 wir\$6 circuit) near (substrate board)) pwb pcb) and (((chip die dice) near2 stack\$6) with (spacer standoff interposer separat\$6))	US-PGPUB; USPAT; FPRS; EPO; JPO; DERWENT	OR	ON	2008/01/15 20:00
L11	113	(((((packag\$6 wir\$6 circuit) near (substrate board)) pwb pcb) and ((chip die dice) near2 stack\$6) and (spacer standoff interposer separat\$6)).clm.	US-PGPUB	OR	ON	2008/01/15 20:23
L12	83	(((((packag\$6 wir\$6 circuit) near (substrate board)) pwb pcb) and ((chip die dice) near2 stack\$6) and (spacer standoff interposer separat\$6) and (active semiconductor\$6 integrated ic)).clm.	US-PGPUB	OR	ON	2008/01/15 20:45
L13	1	"20050224943".pn.	US-PGPUB; USPAT	OR	OFF	2008/01/15 20:43

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L14	53	((packag\$6 wir\$6 circuit substrate board pwb pcb) and ((chip die dice) near2 stack\$6) and (spacer standoff interposer separat\$6) and (active semiconduct\$6 integrated ic) and ((first second third fourth) near (side edge boundary))).clm.	US-PGPUB	OR	ON	2008/01/15 20:48
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